AMENDMENT TRANSMITTAL LETTER (Large Entity) Docket No Applicant(s): Rajeev Joshi et al. 11948.0021 **Group Art Unit** Application No. Filing Date Examiner Customer No. Confirmation No. 10/618,113 July 11, 2003 David A. Zarnecke 27966 2829 8697 WAFER-LEVEL CHIP SCALE PACKAGE AND METHOD FOR FABRICATING AND USING THE SAME **COMMISSIONER FOR PATENTS:** ransmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below. CLAIMS AS AMENDED **NUMBER EXTRA ADDITIONAL** HIGHEST# **CLAIMS REMAINING** RATE CLAIMS PRESENT AFTER AMENDMENT PREV. PAID FOR FEE \$0.00 **TOTAL CLAIMS** 29 33 Х \$50.00 \$0.00 0 \$200.00 INDEP. CLAIMS \$0.00 Multiple Dependent Claims (check if applicable) \$0.00 TOTAL ADDITIONAL FEE FOR THIS AMENDMENT No additional fee is required for amendment. Please charge Deposit Account No. in the amount of to cover the filing fee is enclosed. A check in the amount of The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account Any additional filing fees required under 37 C.F.R. 1.16. Any patent application processing fees under 37 CFR 1.17. Payment by credit card. Form PTO-2038. WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038. Dated: Signature Kenneth E. Horton **Attorney for Applicants** I hereby certify that this correspondence is being deposited with Registration No. 39,481 the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, Kirton & McConkie P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on 60 East South Temple Ste. 1800 Salt Lake City, Utah 84111 (801) 321-4897 (801) 321-4893 fax Signature of Person Mailing Correspondence **Erin Cowles** CC:

Typed or Printed Name of Person Mailing Correspondence

Serial No. 10/618,113 Attorney Docket No. 11948.0021



Rapey re Patent Application: Rajeev Joshi et al.

Serial No.: 10/618,113

Filed: July 11, 2003

For: Wafer-Level Chip Scale Package and Method for Fabricating and Using The

SAME

Confirmation No. 8697

Group Art Unit: 2829

Examiner: Zarnecke, David A.

Mail Stop Non-Final Response Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

AMENDMENT AND REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. §§ 1.111 AND 1.116

In response to Notice of Non-Compliant Amendment mailed on June 9, 2005, and in further response to the Office Action mailed February 1, 2005, Applicant requests reconsideration of this application in light of the following amendments and remarks.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on this \(\begin{aligned} \text{\$\psi} \\ \text{day of } \end{aligned} \)

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6/16/2005